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... dead droid record droid work attack **clone** droid episode ii r2d2 star war ...

pic oopic pic **microcontroller** otp **microcontroller** emulator **microcontroller** ...

[www.oopic.com/](http://www.oopic.com/) - 14k - [Cached](#) - [Similar pages](#)

### entry.html

... as it is slow to use an **emulator** and difficult to configure. ... the SCC

chipset uses a different style of communication familiar in the PC **clone** world. ...

[www.hostultra.com/~darknerd/tech68k/entry.html](http://www.hostultra.com/~darknerd/tech68k/entry.html) - 17k - [Cached](#) - [Similar pages](#)

### The PICList

... This says that a PIC **microcontroller** (or **clone** like the SX line) is directly

... or for an embedded controller (like a programmer or a simulator etc. ...

[www.piclist.com/techref/piclist/listfaq.htm](http://www.piclist.com/techref/piclist/listfaq.htm) - 41k - May 10, 2005 - [Cached](#) - [Similar pages](#)

### FreeRTOS - Processor RTOS Ports

... A GCC ARM7 port for the AT91 **microcontroller**. The demo is preconfigured to run on the Embest ATEB40X prototyping board (Atmel AT91EB40A **clone**). ...

[www.freertos.org/a00090.html](http://www.freertos.org/a00090.html) - 11k - [Cached](#) - [Similar pages](#)

### PIC ICD IN CIRCUIT DEBUGGER

IC-ICD2 PIC MICROCONTROLLER IN-CIRCUIT DEBUGGER. DESCRIPTION: Low cost development tool providing real time emulation for all PICs with build-in ICD feature ...

[www.olimex.com/dev/pic-icd2.html](http://www.olimex.com/dev/pic-icd2.html) - 2k - May 9, 2005 - [Cached](#) - [Similar pages](#)

### Hierarchical System-on-Chip cosimulation and emulation now ...

... Virtual Component of the **microcontroller** without making its **emulator** obsolete.

... **Clone**", or Dolphin's POC, to give life to Raisonance's novel **Emulator** ...

[www.us.design-reuse.com/news/?id=848&print=yes](http://www.us.design-reuse.com/news/?id=848&print=yes) - 28k - [Cached](#) - [Similar pages](#)

### Embedded

... Rabbit Semiconductor, A **clone** Z80 **microcontroller**, C, Cheap development kit and

... you would have an **emulator** which you can plug in the **microcontroller** ...

[homepages.which.net/~paul.hills/Embedded/EmbeddedBody.html](http://homepages.which.net/~paul.hills/Embedded/EmbeddedBody.html) - 57k - [Cached](#) - [Similar pages](#)

### Digital Laboratory

... GSM Modem **Emulator** / PhoneBookEditor (take also a look at the Links and resources)







... Attack an a PIC 16C71 **microcontroller** (Dejan Kaljevic) ...

www.digital-laboratory.de/home.htm - 46k - May 9, 2005 - [Cached](#) - [Similar pages](#)

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### CPU-World: Intel 8048 (MCS-48) **microcontroller** family

... The **microcontroller** marked with HP part number 1820-2269, Intel P8048 ... Czechoslovakian **clone** of Intel 8048 **microcontroller**, Tesla MHB8048 ...

[www.cpu-world.com/CPU/8048/](http://www.cpu-world.com/CPU/8048/) - 20k - [Cached](#) - [Similar pages](#)

### CPU-World: Intel 8051 **microcontroller** family

Intel 8051 **microcontroller** family - information, pictures, identification, pinouts and support ... Soviet **clone** of Intel 8051AH **microcontroller**, KR1816VE51 ...

[www.cpu-world.com/CPU/8051/](http://www.cpu-world.com/CPU/8051/) - 17k - [Cached](#) - [Similar pages](#)

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#### The Microcontroller Shop

Emulators, Debuggers, Programmers  
for all major micro architectures  
[microcontrollershop.com](http://microcontrollershop.com)

### PICmicro® studio - intermediate projects

... Set of chips 93LC56 (EEPROM memory) and PIC16F84 (**microcontroller**), this last one **clone** of the popular BASIC Stamp 1. All the instructions run except ...

[www.electronicaestudio.com/en/intermedios.htm](http://www.electronicaestudio.com/en/intermedios.htm) - 14k - [Cached](#) - [Similar pages](#)

### PICmicro® studio - advanced projects

... to program the PIC® **microcontroller**, a timer and internet addresses. ... Insert the memory in the base socket and the empty memory in the **clone** socket. ...

[www.electronicaestudio.com/en/picmaster.htm](http://www.electronicaestudio.com/en/picmaster.htm) - 24k - [Cached](#) - [Similar pages](#)

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## MICROCONTROLLERS AND EMBEDDED PROCESSORS

... A typical **microcontroller** application is the monitoring of my house. ... A common example of a general purpose computer, would be a typical PC **clone**. ...

[www.esacademy.com/automation/faq/primer/2.htm](http://www.esacademy.com/automation/faq/primer/2.htm) - 10k - [Cached](#) - [Similar pages](#)

### The PICList

... This says that a PIC **microcontroller** (or **clone** like the SX line) is directly involved, connected or the entire subject of your post. ...

[www.piclist.com/techref/piclist/](http://www.piclist.com/techref/piclist/) - 27k - May 9, 2005 - [Cached](#) - [Similar pages](#)

### FreeRTOS - A FREE open source RTOS for small embedded real time ...

Compare microcontrollers - a **microcontroller** performance comparison ... instruction set **clone**), PIC, AVR, Rabbit and RDC (AMD 186 **clone**) microcontrollers. ...

[www.freertos.org/PC/](http://www.freertos.org/PC/) - 3k - [Cached](#) - [Similar pages](#)

### A wireless color-changing decoration project

... I figured I could use a Parallax Basic Stamp I **microcontroller** (\$34), ... The PIC16F84A **microcontroller** is a favorite among hobbyists (even after being ...

[students.washington.edu/natetrue/wlcolor/wlcolor.html](http://students.washington.edu/natetrue/wlcolor/wlcolor.html) - 14k - May 9, 2005 -

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### OOPic Home Page

How to use the OOPIC **microcontroller**. OOPic is an acronym for Object-Oriented

Programmable Integrated Circuit.

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### HITACHI GLOBAL : News Releases from Headquarters : Sep 20, 2001

... Hitachi Releases **SoC Emulator** Incorporating SH2-DSP Core ...  
 2001 -- Hitachi, Ltd. (TSE: 6501) today announced the **SoC Emulator**  
 as a development tool ...

[www.hitachi.com/New/cnews/E/2001/0920/](http://www.hitachi.com/New/cnews/E/2001/0920/) - 13k -

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#### [PDF] Hitachi Releases **SoC Emulator** Incorporating SH2-DSP Core

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... Ltd. (TSE: 6501) today announced the **SoC Emulator** as a  
 development tool ... The present **SoC Emulator** has been  
 designed to meet this need. ...

[www.hitachi.com/New/cnews/E/2001/0920/0920.pdf](http://www.hitachi.com/New/cnews/E/2001/0920/0920.pdf) -

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#### SoC Development Tools

Raisonance provides Tools for  
**SoC (software and emulator)**

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### EDApability

... The **SoC Emulator** Example is completely based on Java. ... **Emulator** is directly  
 derived from you **SoC** RTL/gate code (VHDL and/or Verilog) and can be ...

[www.edaptability.com/emulator.htm](http://www.edaptability.com/emulator.htm) - 15k - [Cached](#) - [Similar pages](#)

### [PDF] IMS SoC-Emulator

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... The Fraunhofer-IMS **SoC-Emulator** offers ... **IMS SoC-Emulator**. **SoC Emulator**.  
 ASIC. Rapid prototyping with the **IMS SoC Emulator**. Fraunhofer-Institut für ...

[www.ims.fraunhofer.de/datenblaetter/intellectual\\_properties/emulat/emulat-e.pdf](http://www.ims.fraunhofer.de/datenblaetter/intellectual_properties/emulat/emulat-e.pdf) -

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#### Datenblätter / Data sheets Fraunhofer Institut Mikroelektronische ...

... The Fraunhofer-IMS **SoC-Emulator** offers fast prototyping of micro-controller based  
 ... Screenshot of the **SoC Emulator** graphical user interface.

[www.ims.fraunhofer.de/datenblaetter/intellectual\\_properties/emulat/emulat-e.html](http://www.ims.fraunhofer.de/datenblaetter/intellectual_properties/emulat/emulat-e.html) -

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### Mit **SoC-Emulator** Chips nachbilden und testen - [ [Translate this page](#) ]

Selbst die Kaffeemaschine fängt bald an zu »denken«. Zumindest zeichnet sich ein  
 klarer Trend ab, dass alle möglichen Elektrogeräte mit Chips ausgestattet ...

[www.innovations-report.de/html/berichte/informationstechnologie/bericht-14383.html](http://www.innovations-report.de/html/berichte/informationstechnologie/bericht-14383.html) -

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### **Emulator** tackles complex **SoC** verification: News from Cadence ...

The new-generation Incisive Palladium II acceleration/emulation system delivers up to  
 twice the speed and capacity of the first-generation Cadence Palladium ...

[www.electronicstalk.com/news/cad/cad250.html](http://www.electronicstalk.com/news/cad/cad250.html) - 12k - [Cached](#) - [Similar pages](#)

**Emulator offers accelerated SoC verification: News from Cadence ...**

Cadence Design Systems has released an advanced verification environment for the industry-leading processor-based Incisive Palladium acceleration/emulation ...  
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**EPI's new emulator targets SoC devices**

Press Release - New Emulator Targets SoC Devices. ... EPI's new MAJIC emulator will support a large variety of processor and DSP cores, application-specific ...  
[www.epitools.com/about/press\\_releases/majic\\_by\\_name\\_pr.php](http://www.epitools.com/about/press_releases/majic_by_name_pr.php) - 14k - [Cached](#) - [Similar pages](#)

**CybraTech SOC-4000 Emulators**

... Software products work with all emulators that support the CybraTech SOC-4000. If the emulator you use is not listed on this page, please contact your ...  
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### Spec for an XC2S600E based ATX **FPGA**-PC Board

... I see no collision for my PDP-10 or any other **cloner**. ... Internal Configuring from Devices via the **FPGA** by **Microcontroller** ...

neil.franklin.ch/Projects/FPGA-PC/BoardSpec600E.html - 115k - May 9, 2005 - [Cached](#) - [Similar pages](#)

### [PDF] CipherStream Protocol: How CoolRunner-II CPLDs Protect **FPGA** IP v1 ...

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... It doesn't usually take very long to create an **FPGA** design. ... let's consider how it may be attacked by a "cloner." Just to reiterate, we are assuming ...

www.xilinx.com/bvdocs/whitepapers/wp197.pdf - May 9, 2005 - [Similar pages](#)

### Dvd - Dvd Firmware

... embedded **microcontroller** or microprocessor hardware, digital circuitry, ... Electronics Design Services firm providing ASIC, **FPGA**, printed circuit board ...

www.entiredvd.com/dvdfirmware/ - 35k - [Cached](#) - [Similar pages](#)

### MacSurfer's Headline News

... to improve performance" IBM **Microcontroller** Applications Group 10:39 AM ...

"DesignWorks Lite 4.5.4 - Front-end for PCB design, **FPGA** design, and more. ...

www.macsurfer.com/archives/months/january/January9.html - 97k - [Cached](#) - [Similar pages](#)

### 1MW 1 Microsoft Way 1TBS 1 True Brace Style 21D teach yourself ...

... Scanner AMBA Advanced **Microcontroller** Bus Architecture AMD Active Matrix ...

Line Utility CluClo CLUster **CLoner** CLUE Command Line User Environment CLUGS ...

personalpages.tds.net/~slambo/realacro.txt - 275k - [Cached](#) - [Similar pages](#)

### RÉTIF : Répertoire terminologique 2000 - [ [Translate this page](#) ]

... Note : Un cosmide ne permet de **cloner** que des fragments d'ADN de grande taille.

... field programmable gate array (**FPGA**), circuit intégré prédiffusé ...

www-rocq.inria.fr/qui/Philippe.Deschamp/RETIF/20000922-tout.html.gz - 1274k - [Cached](#) - [Similar pages](#)

### [initial revision from uuu-devel@unununium.org--unununium-2004 ...

... EM\_FX66 66 /\* Siemens FX66 **microcontroller** \*/ +#define EM\_ST9PLUS 67 ...

parent as the **cloner** \*/ +#define CLONE\_THREAD 0x00010000 /\* Same thread group? ...

unununium.org/darcs/uuu/\_darcs/patches/

20041125041506-ce11a-542af47bf576a94702b4ff7b466db4c329f9108e.gz - 2247k - [Cached](#) - [Similar pages](#)

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### VHDL / FPGA Digital Design Engineer

... of an **FPGA** and testbench interfacing an Infineon 16 bit **microcontroller** to 24

... Design and implementation of a **reverse engineering** tool, utilizing an ...

[www.cambridgenetwork.co.uk/pooled/articles/BF\\_CVART/view.asp?Q=BF\\_CVART\\_130711](http://www.cambridgenetwork.co.uk/pooled/articles/BF_CVART/view.asp?Q=BF_CVART_130711) - 26k -

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### [PDF] Controlling FPGA Configuration with a Flash-Based MCU Application ...

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... the circuit realized with the **FPGA** is very difficult to **reverse- engineer**.

When using a **microcontroller** to configure the **FPGA**, addi- ...

[www.eetasia.com/ARTICLES/2000AUG/2000AUG28\\_PL\\_MEM\\_CT\\_AN.PDF](http://www.eetasia.com/ARTICLES/2000AUG/2000AUG28_PL_MEM_CT_AN.PDF) - [Similar pages](#)

### EverythingPCB : Engineering Services / Systems Integration ...

... Firmware (microprocessor, **microcontroller**, **FPGA**, or other programmable ...

**Reverse-engineering** existing boards \* Artwork conversion from paper or film ...

[www.everythingpcb.com/p10101.htm](http://www.everythingpcb.com/p10101.htm) - 26k - [Cached](#) - [Similar pages](#)

### VirtexTools FPGA Programming and Debugging Tools Project

... This project grow out of my PDP-10 Clone Microprocessor in **FPGA** project. ...

just to **reverse engineer** their chips and make VirtexTools, ...

[neil.franklin.ch/Projects/VirtexTools/](http://neil.franklin.ch/Projects/VirtexTools/) - 23k - May 9, 2005 - [Cached](#) - [Similar pages](#)

### [doc] Resume: James W

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... Translated existing **FPGA** designs to faster technologies. ... **Reverse Engineered**

VME NNP Board for which all original driver and C Code was lost. ...

[www.sss-mag.com/txt/bradresume2004B.doc](http://www.sss-mag.com/txt/bradresume2004B.doc) - [Similar pages](#)

### CommsDesign - Building secure networks for comms work

... a bit stream from a PROM or sent to the **FPGA** via an on-board **microcontroller**.

... To **reverse engineer** such a device an **engineer** would have to determine ...

[www.commsdesign.com/design\\_corner/showArticle.jhtml?articleID=16501843](http://www.commsdesign.com/design_corner/showArticle.jhtml?articleID=16501843) - 47k - [Cached](#) - [Similar pages](#)

### Ronald S. Bader - Systems Engineering/ Electronics Design ...

... Embedded microprocessor / **microcontroller**, **FPGA** / CPLD design; mixed signal

processing; ... Design analysis and **reverse engineering**; Expert Witness ...

[www.patca.org/asp/detail2.asp?RecordID=3](http://www.patca.org/asp/detail2.asp?RecordID=3) - 23k - [Cached](#) - [Similar pages](#)

### Breaking copy protection in microcontrollers

... how particular **microcontroller** or smartcard work is to **reverse engineer** it.

... Sometimes this devices are used for complex projects in parallel to **FPGA** ...

[www.cl.cam.ac.uk/~sps32/mcu\\_lock.html](http://www.cl.cam.ac.uk/~sps32/mcu_lock.html) - 48k - [Cached](#) - [Similar pages](#)

### [PDF] Structured eASIC security whitepape J Park 4 12 05 f.indd

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... ASIC and field programmable gate arrays (**FPGA**), are rapidly capturing market

... the risk of IP piracy is still menacing through **reverse engineering**. ...

[www.easic.com/technology/Achieving\\_Design\\_Security\\_Requirements\\_using\\_eASIC\\_Technology.pdf](http://www.easic.com/technology/Achieving_Design_Security_Requirements_using_eASIC_Technology.pdf) - [Similar pages](#)

**[PDF] Crypto Blaze : 8-Bit Security Microcontroller**







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... difficult to **reverse engineer**, if properly managed. These qualities are attractive to the ... was developed by Ken Chapman (Xilinx) for **FPGA** products. ... [www.xilinx.com/bvdocs/appnotes/xapp374.pdf](http://www.xilinx.com/bvdocs/appnotes/xapp374.pdf) - [Similar pages](#)

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